1   What is claimed is:

1   1. An apparatus comprising:

2   an input/output (I/O) device;

3   said I/O device being operative to receive a fragment of electronic data, and further being

4   operative to identify at least a portion of the contents of said fragment of electronic data, and

5   further being operative to moderate one or more interrupts of an associated computing platform

6   processor, based at least in part on the at least a portion of said contents.

1   2. The apparatus of claim 1, wherein the at least a portion of said contents comprises an

2   acknowledgement (ACK).

1   3. The apparatus of claim 1, wherein said I/O device comprises a network interface card (NIC).

1   4. The apparatus of claim 1, wherein at least a portion of said contents comprises a priority

2   designation.

1   5. The apparatus of claim 1, wherein said I/O device is configured to moderate by substantially

2   immediately asserting said one or more interrupts of said associated computing platform

3   processor.

1   6. The apparatus of claim 1, wherein said I/O device is configured to moderate by deferring

2   said one or more interrupts of said associated computing platform processor so that a

3   predetermined number of interrupts per unit of time is not exceeded.

1 7. The apparatus of claim 1, wherein said I/O device is configured to moderate by deferring

2 said one or more interrupts until a particular number of fragments of electronic data of a

3 particular type are received by said I/O device.

1 8. The apparatus of claim 1, wherein said I/O device is configured to moderate by deferring

2 said one or more interrupts until a particular quantity of electronic data is received.

1 9. The apparatus of claim 1, wherein said moderation of associated computing platform

2 interrupt scheme is configurable through a user interface.

1 10. The apparatus of claim 1, and further comprising:

2 said I/O device further being operative to measure a particular period of time after the

3 receipt of a fragment of electronic data, and further being operative to moderate one or more

4 interrupts of an associated computing platform after said particular period of time has elapsed.

1 11. A method of moderating one or more interrupts of an associated computing platform

2 comprising:

3 receiving a fragment of electronic data;

4 identifying, at least partially, the contents of said fragment of electronic data; and

5 moderating said one or more interrupts based at least in part on said at least partially

6 identified contents.

1 12. The method of claim 11, wherein said at least partially identified contents comprises an

2 acknowledgement (ACK).

1   13. The method of claim 11, wherein said at least partially identified contents comprises a

2   priority designation.

1   14. The method of claim 11, wherein said moderating comprises substantially immediately

2   interrupting said associated computing platform processor.

1   15. The method of claim 11, wherein said moderating comprises deferring said one or more

2   interrupts of said associated computing platform processor if a predetermined number of

3   interrupts per unit time is met or exceeded.

1   16. The method of claim 11, wherein said moderating comprises deferring said one or more

2   interrupts until a particular number of fragments of electronic data of a particular type are

3   received.

1   17. The method of claim 11, wherein said moderating comprises deferring said one or more

2   interrupts until a particular quantity of electronic data is received.

1   18. The method of claim 11, wherein said moderating is configurable through a user interface.

1   19. The method of claim 11, and further comprising:

2   measuring a particular period of time after the receipt of a fragment of electronic data; and

3   performing said moderating after said particular period of time has elapsed.

1   20. An article comprising:

2   a storage medium;

3   said storage medium having stored thereon instructions, that when executed by a

4   computing platform, result in execution of a method of processing latency sensitive electronic

5   data comprising:

6   receiving a fragment of electronic data;

7   at least partially identifying the contents of at least a portion of said fragment of electronic

8   data; and

9   moderating said one or more interrupts based at least in part on said at least partially

10 identified contents.

1   21. The article of claim 20, wherein said at least partially identified contents comprises an

2   acknowledgement (ACK).

1   22. The article of claim 20, wherein said at least partially identified contents comprises a priority

2   designation.

1   23. The article of claim 20, wherein said moderating comprises substantially immediately

2   interrupting said associated computing platform processor.

1   24. The article of claim 20, wherein said moderating comprises deferring said interrupting of

2   said associated computing platform processor.

1   25. The article of claim 20, wherein said moderating comprises deferring said one or more

2   interrupts until a particular number of fragments of electronic data of a particular type are

3   received.

1   26. The article of claim 20, wherein said moderating comprises deferring said one or more

2   interrupts until a particular quantity of electronic data is received.

1   27. The article of claim 20, wherein said moderating is configurable through a user interface.

1   28. The article of claim 20, and further comprising:

2   measuring a particular period of time after the receipt of a fragment of electronic data; and

3   performing said moderating after said particular period of time has elapsed.